

Scholar Details:

		Internal Scholars				
S.No	Candidate Name & Designation	Provisional Details		Title of the Research Work	No. of Paper Published in Annexure- I	Present status of the PhD (Doing coursework/ Comprehensive viva completed/ Synopsis submitted/ Thesis submitted)
		Year of Reg.	Reg. No.			
1.	Ms.K.Saranya, Assistant Professor	06.06.2016	16244697151	ASIC implementation of Efficient reversible arithmetic processing units for signal processing and cryptographic applications		Comprehensive viva completed